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	09/955,328	Joseph W. Tiece, et al.	
	Docket Number	Group Art Unit	Filing Date
	068354.0169	2115	September 18, 2001

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.						
B.						
C.						
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FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
C	I. 88/09008	11/17/88	WO	G06F	9/44	X	
C	J. 0999500	5/10/00	EP	G06F	12/08	X	
	K.						
	L.						
	M.						
	N.						
	O.						
	P.						

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author Title Source and Pertinent Pages)	DATE
C	Q. Microchip Technology, Inc., "High-Performance ROM-less Microcontrollers with External Memory Bus", XP-002264754, pages 1-83	2001
C	R. Microchip Technology, Inc., "Silicon/Data Sheet Errata" PIC18C601/801, XP002264897, pages 1-10	2002
C	S. Microchip Technology, Inc., "Implementing the External Memory Interface on PIC18C601/801 MCUs" XP002264755, pages 1-32	2001
C	T. Microchip Technology, Inc., Web page about application note AN778 at <http://www.microchip.com/1010/suppdoc/appnote/all/an>, 1 page, XP002264898	11/19/03
C	U. PCT search report for PCT/US02/28501, 9 pages	Mailed 4/2/04

EXAMINER	DATE CONSIDERED
<i>CAO</i>	<i>11/28/04</i>

EXAMINER: Initial if citation considered whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.